KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.
काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015, తెలంగాణ రాష్ట్రం, భారతదేశం
Office of the Controller of Examinations

for SUPPLEMENTARY

2022 Admitted Batches

Date: 02-04-2024

No. URR18R22- B.Tech. III-Sem. /ESE Supply Schedule /2024

End Semester Examinations (ESE) of URR-18R22 B.Tech. III-Semester (SUPPLEMENTARY) APRIL/MAY, 2024

TIME TABLE | **Examination Timings: 09:30 AM to 12:30 PM**

Date & Day of Examination	CE	ME	EEE	CSE	IT	ECE	CSN	ECI	CSM (AIML)	CSO (IoT)
25.04.2024 Thursday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
27.04.2024 Saturday	OE-I Strength of Materials	Mechanics of Solids	Electrical Measurements & Instrumentation	Object Oriented Programming through JAVA	----	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
29.04.2024 Monday	Fluid Mechanics	Material Science and Metallurgy	Electromagnetic Fields	Discrete Mathematics	Discrete Mathematics	Signals & Systems	---	Signals Systems and Random Processes	Formal Languages and Automata Theory	Fundamentals of Internet of Things
01.05.2024 Wednesday	Surveying	Engineering Thermodynamics	Network Theory	Computer Architecture and Organization	Computer Architecture and Organization	Network Analysis	Computer Architecture and Organization	Electronic Measurements and Sensors	Computer Organization and Architecture	----
03.05.2024 Friday	Construction Materials	Machine Drawing	Electronic Devices and Circuits	Advanced Data Structures	Advanced Data Structures	Analog Circuits – I	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
06.05.2024 Monday	---	---	--	Digital Electronics	Switching Theory and Logic Design	Switching Theory & Logic Design	Digital Electronics	Digital Circuits and Logic Design	----	---
08.05.2024 Wednesday	---	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	---	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	---	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	---	---	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life
09.05.2024 Thursday	---	---	---	---	Operating Systems	--	Operating Systems	---	Operating Systems	Computer Networks

Candidate should occupy his/her seat atleast <u>five minutes before</u> the commencement of the examination

To: **All Notice Boards / Class WhatsApp Groups**

Copy to:- 1. The Principal for information
2. The Registrar 3. HOD / BoS Chairpersons   4. Dean, Academic Affairs   5. Prof. I/C. Training & Placement 6. Web Team

Controller of Examinations